



# EINLADUNG zum IFP-SEMINAR

## Topoelectrical circuits

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Host: Silke Bühler-Paschen  
Termin: **Mittwoch, 10. Januar 2018, 16:00 Uhr**  
Ort: Institut für Festkörperphysik, TU Wien  
Wiedner Hauptstraße 8-10, 1040 Wien  
Seminarraum DC rot 07 (roter Bereich, 7. OG)  
Förderer: FWF I2535-N27 „TopQuantum“

First developed by Alessandro Volta and Felix Savary in the early 19th century, circuits consisting of resistor, inductor and capacitor (RLC) components are now omnipresent in modern technology. The behavior of an RLC circuit is governed by its circuit Laplacian, which is analogous to the Hamiltonian describing the energetics of a physical system. We show that “topoelectrical” boundary resonances (TBRs) appear in the impedance read-out of a circuit whenever its Laplacian bandstructure resembles that of topological semimetals - materials with extensive degenerate edge modes known as Fermi arcs that also harbor enigmatic transport properties. Such TBRs not only provide unambiguous and highly robust signals for the presence of a topological phase, but also promise diverse applicability within high density electronic mode processing. Due to the versatility of electronic circuits, our topological semimetal construction can be generalized to topoelectrical phases with any desired lattice symmetry, spatial dimension, and even quasiperiodicity. Topoelectrical circuits establish a bridge between electrical engineering and topological states of matter, where the accessibility, scalability, and operability of electronics promises to synergize with the intricate boundary properties of topological phases.